

Scaling Score-P to the next level



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Approaches



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Scalable system tree description

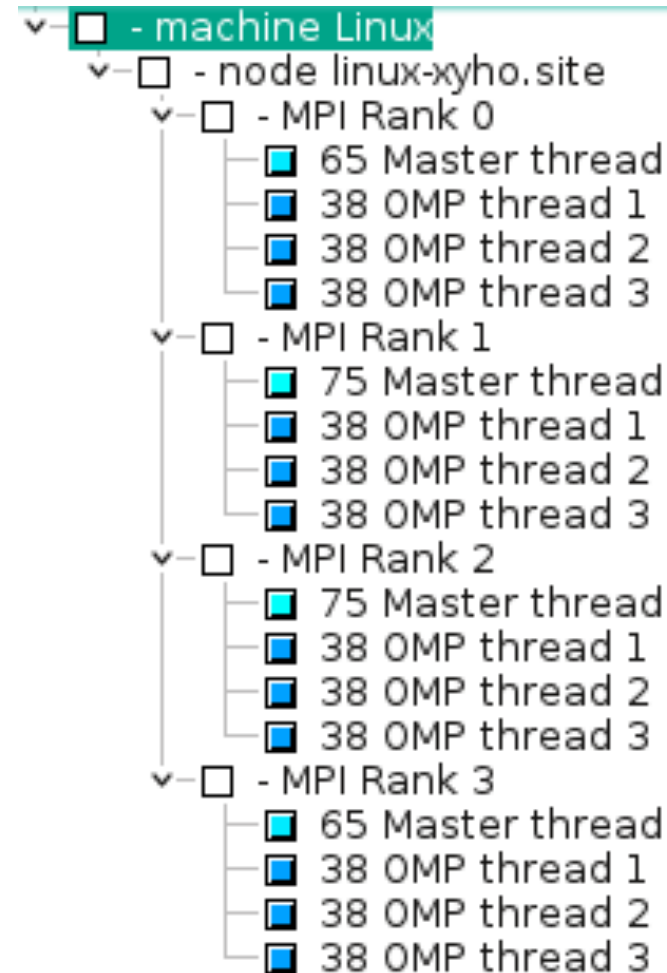
Automatic thread-level aggregation

System tree definitions



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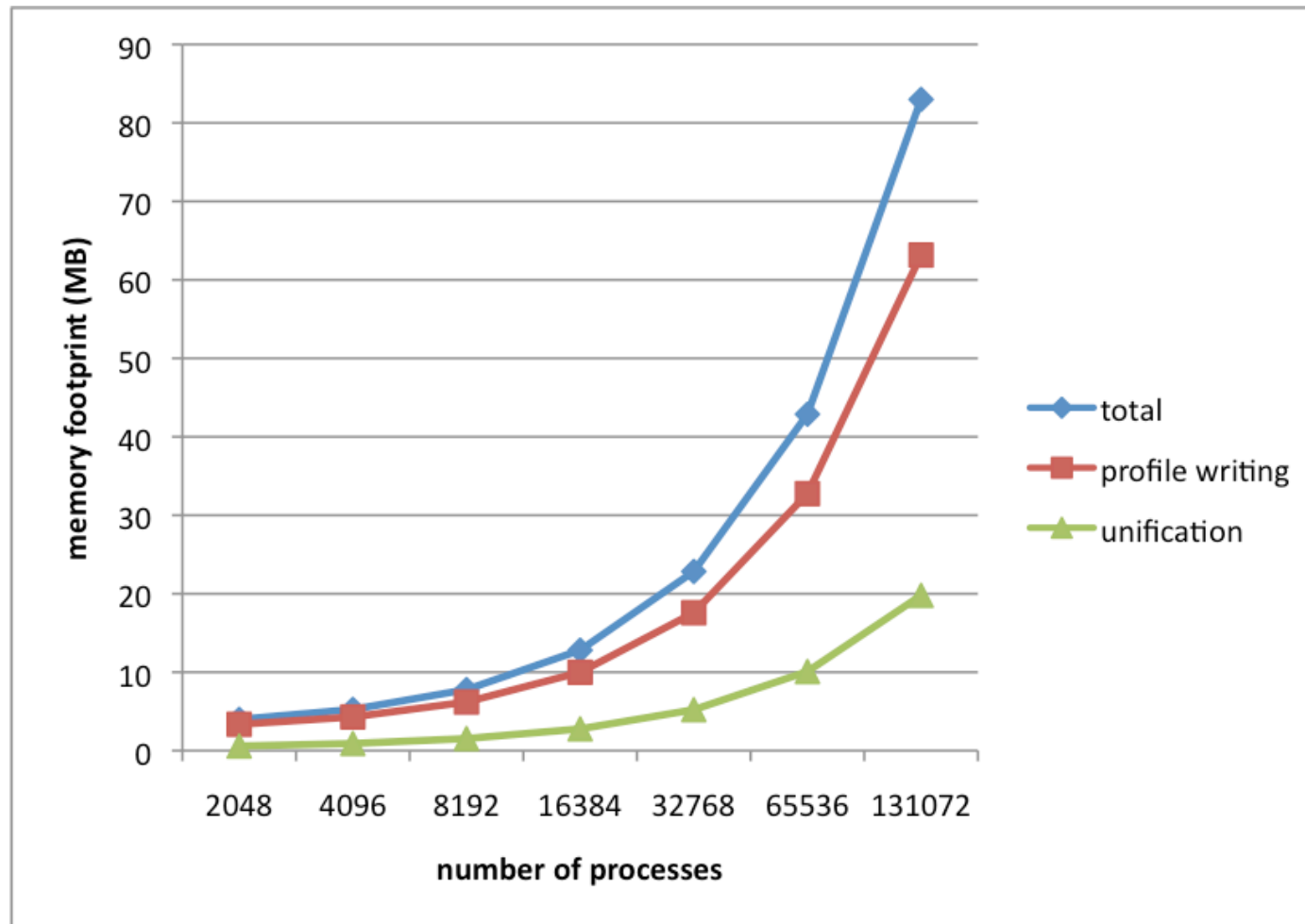
Single-node definitions: One data
record per system tree element



Score-P finalization memory footprint



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The goal



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A system tree description with a memory footprint that does not depend on the system size

An parallel algorithm to create the new system tree description from local information with constant memory footprint

Sequence definitions (1)

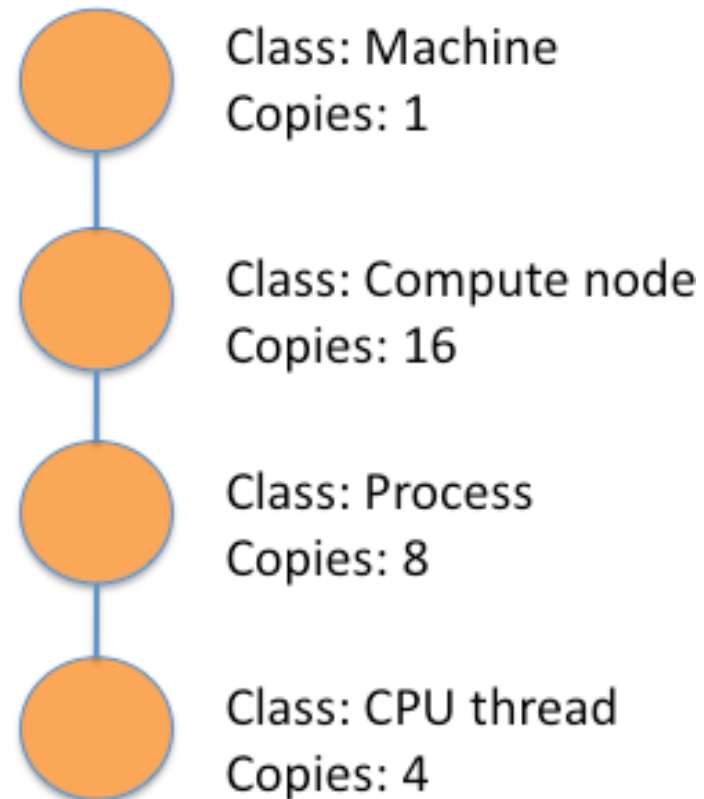


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Based on the PERI-XML proposal
Exploit regularity of systems
Constant size for regular systems

A depth-first traversal of the system
tree provides enumeration

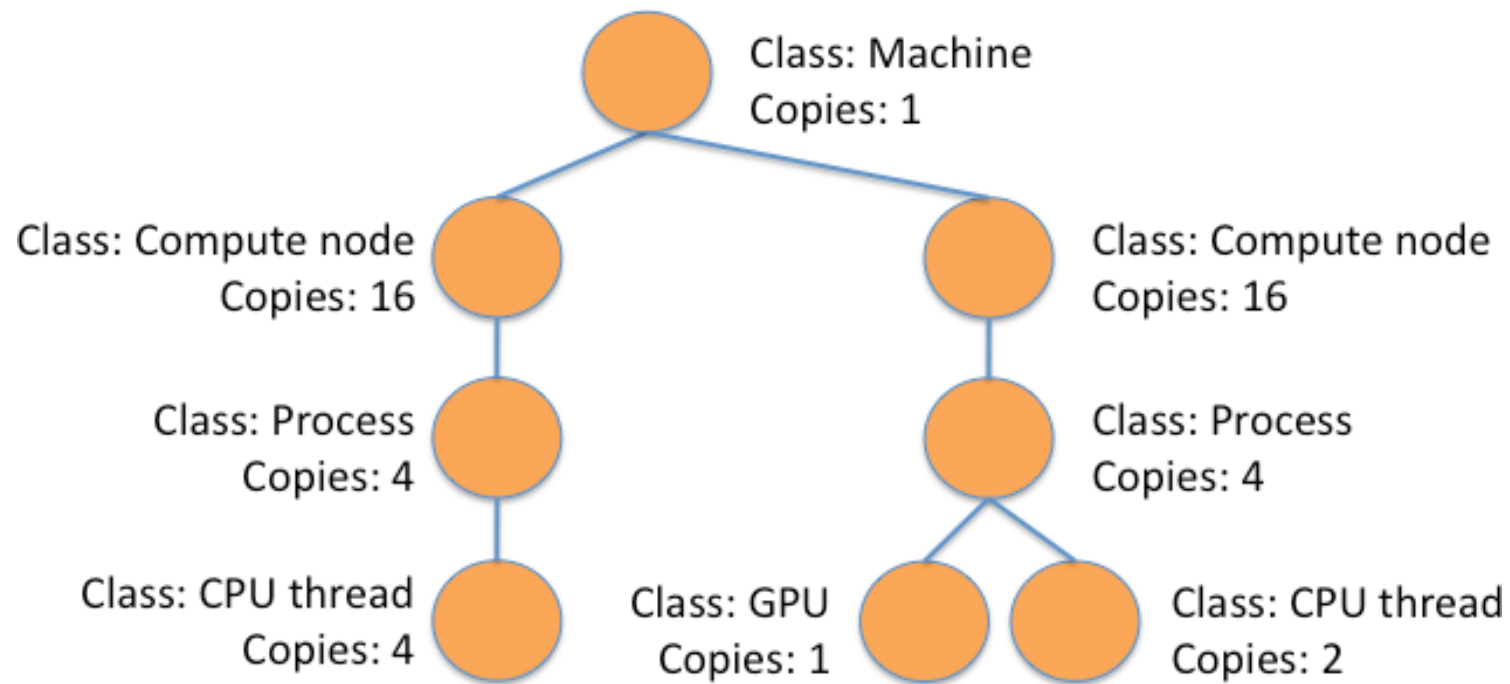
Can be used as index to reference
individual nodes



Sequence definitions (2)



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Memory footprint

$O(\text{size of definitions} + \text{size of communicator})$

- For regular systems: Size of definitions in $O(1)$
- MPI communicators: Memory footprint can be $O(1)$
- Current implementations usually $O(\# \text{ processes})$
- Expect MPI communicator implementations to scale to the system size

Computational complexity



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$$O(\log P * (t_{P2P}(P) + t_{Comm}(P)) * S * D)$$

P: Number of processes

t_{P2P} : Time of a peer-to-peer communication

t_{Comm} : Time for communicator creation

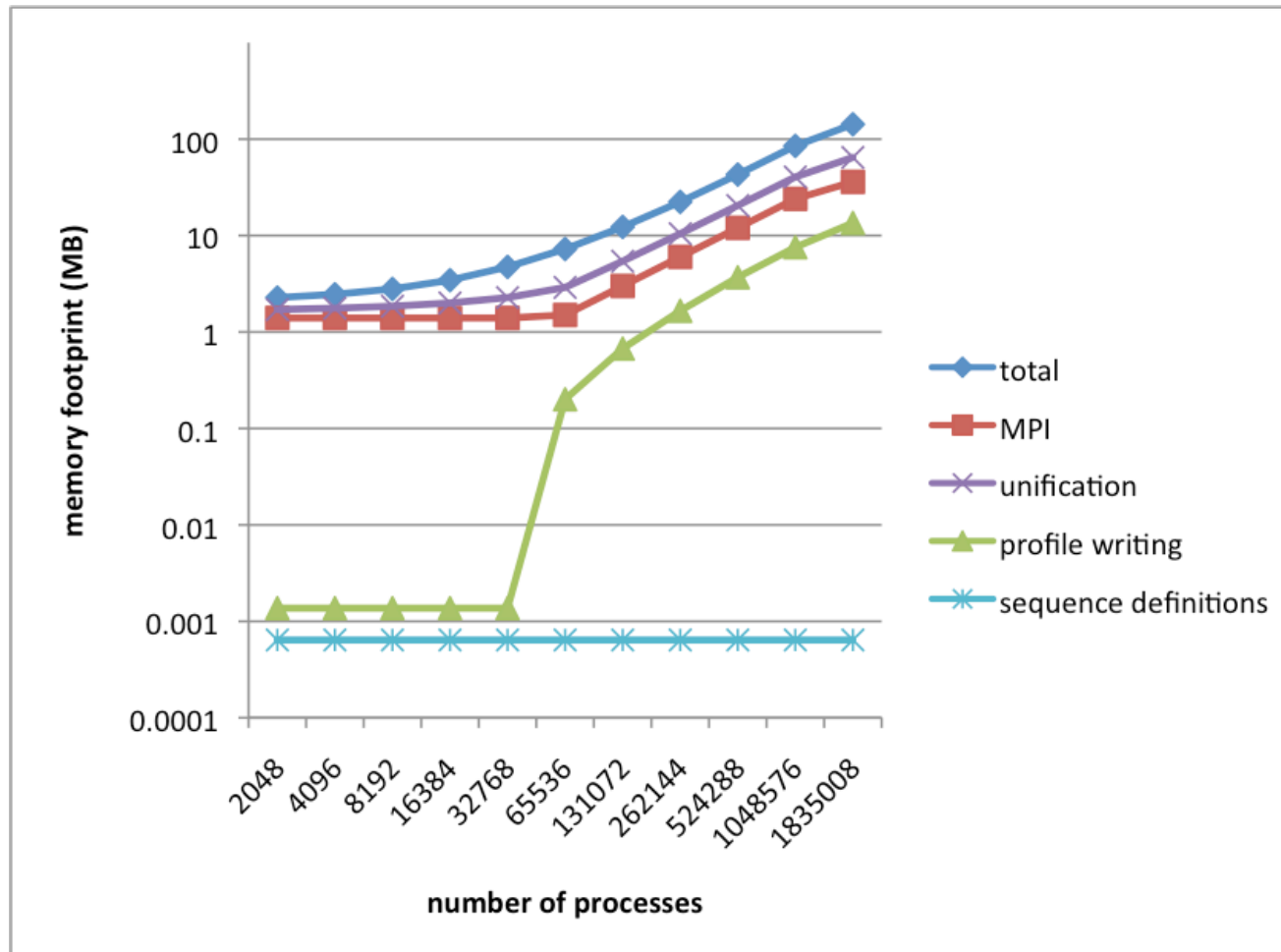
S: Size of the sequence definitions

D: Depth of the system hierarchy

Finalization memory footprint (hello world)



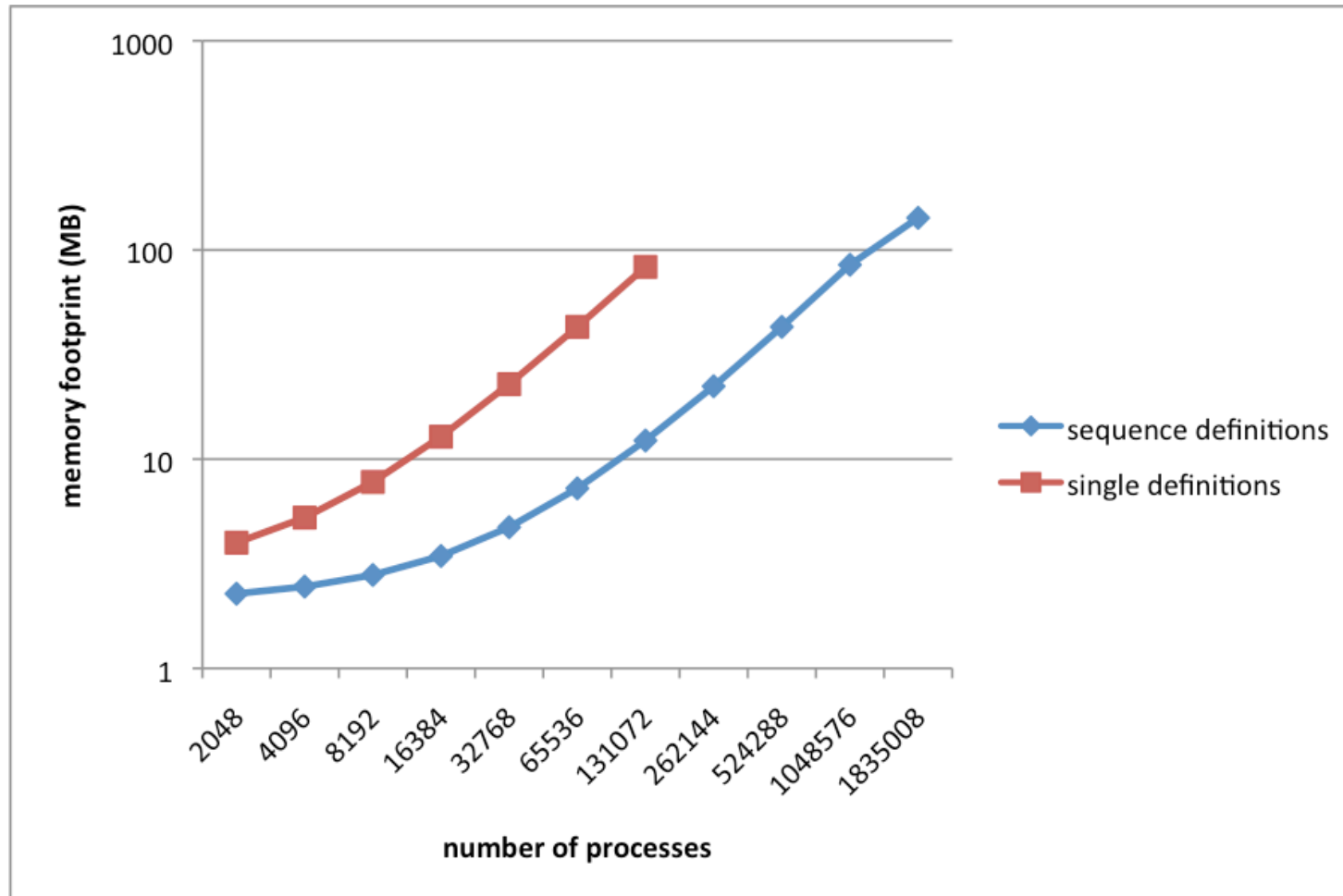
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Memory footprint comparison (hello world)



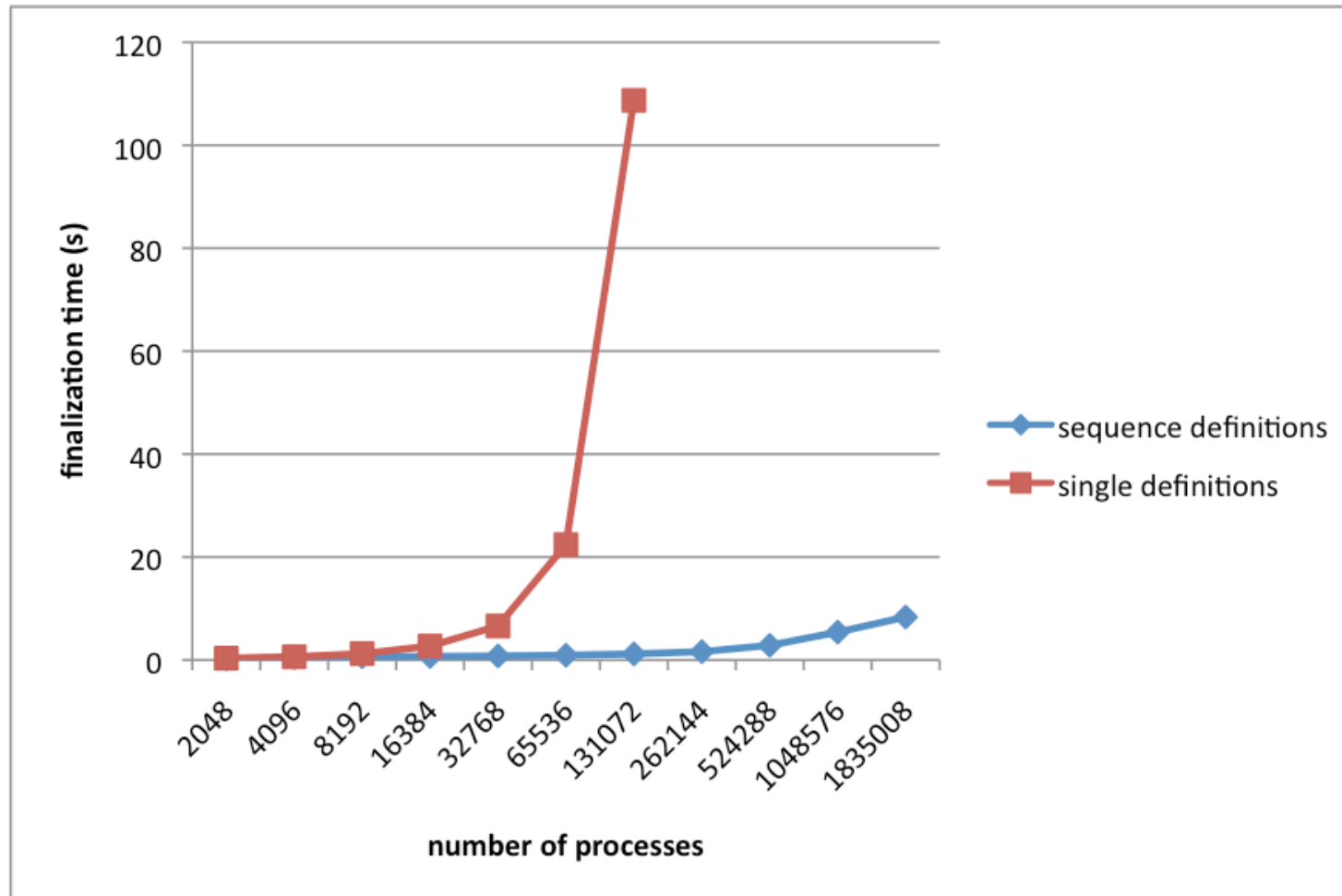
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Finalization time (hello world)



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Thread-level aggregation



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Which data is needed?

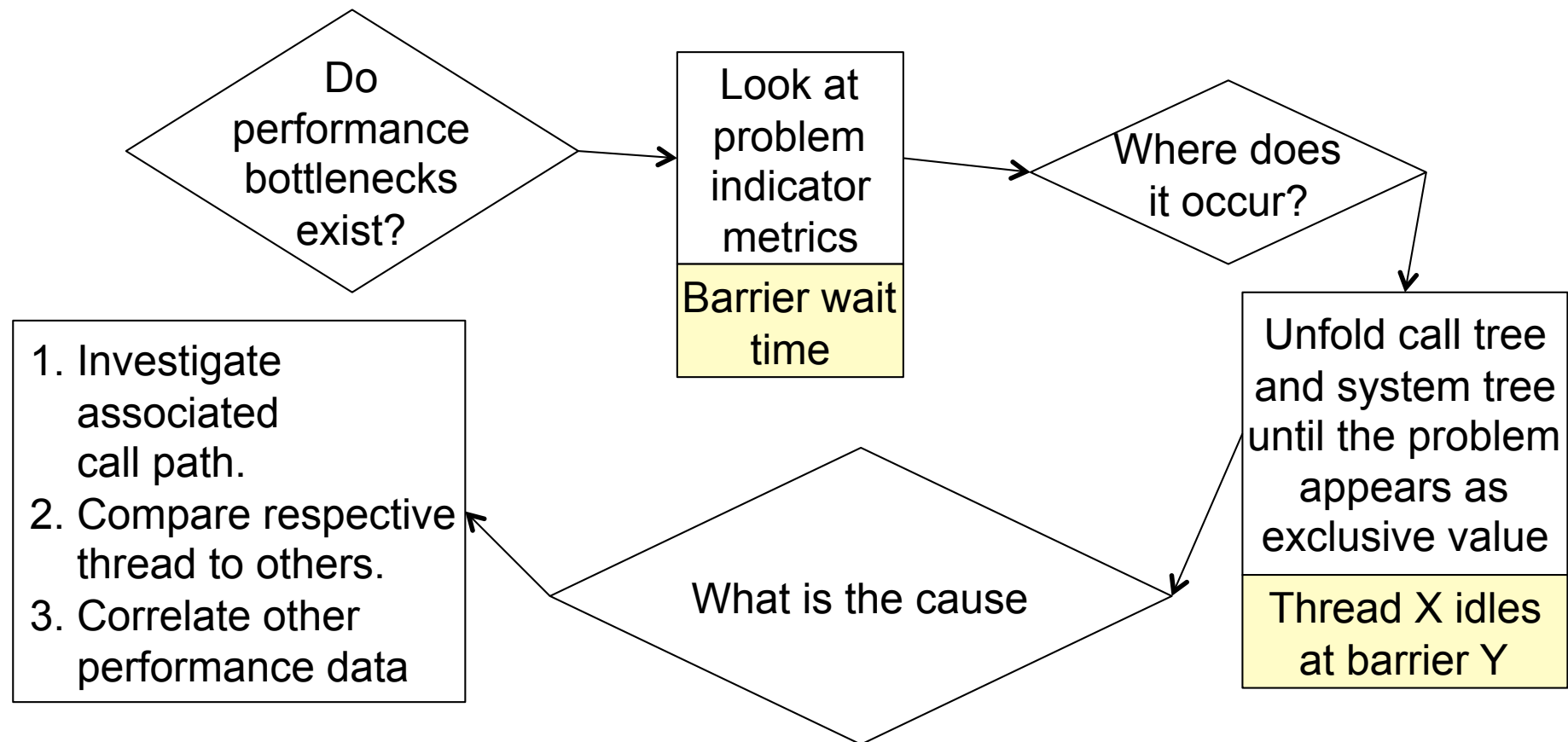
- Analysis workflow with CUBE

The compression strategies

Evaluation of the compression ratio

Evaluation of the information loss

What information is needed?



Aggregation strategies

SUM:

- Aggregates the data of all threads within a process

SET:

- Keeps also statistical data about the value distribution among the threads

KEY:

- Keeps the three so called key threads
- Aggregates all others

CALLPATH:

- Clusters threads that have the same call tree structure
- Aggregates all threads within a cluster.

SET



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Contains:

- Sum
- Minimum
- Maximum
- Sum of squares (to calculate standard deviation)
- Number of threads

No correlations between call path and metrics possible

KEY

Need to improve the performance of slowest thread

You may want to compare it to the other extreme, the fastest thread

The initial thread plays often an distinct role

Aggregate other threads

- They can provide an average value for comparison

Slowest/Fastest calculation

- Classify regions
- Consider measured time in regions that are considered to do work

Aggregate all threads that have the same call tree structure

- Both have at least one visit to a call path
- The number of visits is not compared

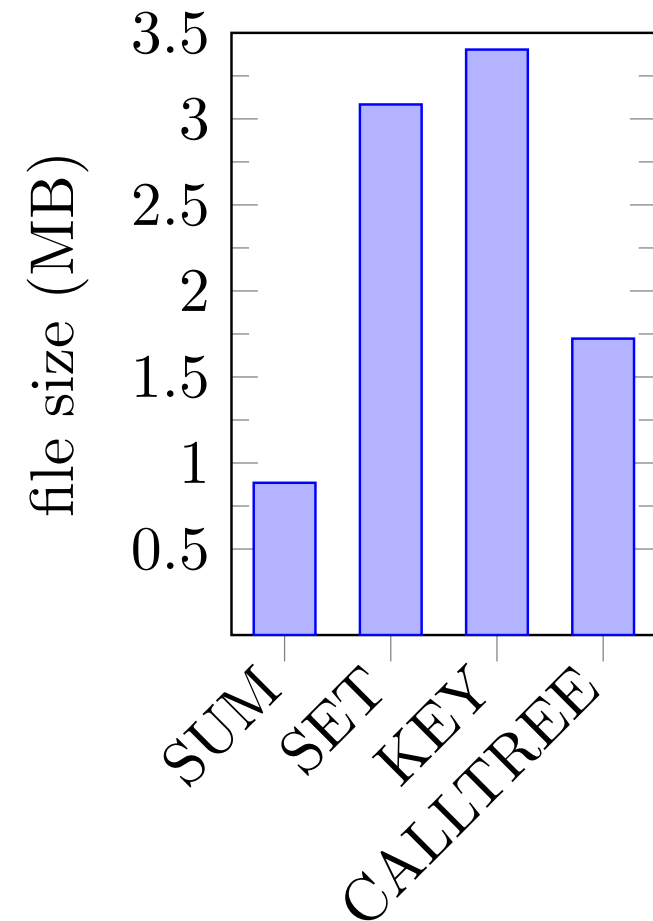
Number of resulting clusters depends on application

- Compression ratio may vary

Compression (1)

Constant files sizes

- Independent of number of threads per process
- Same number of locations stored
- Compression ratio varies with number of threads
- SET a little smaller than KEY because the CUBE record stores number of threads as 32 bit value.



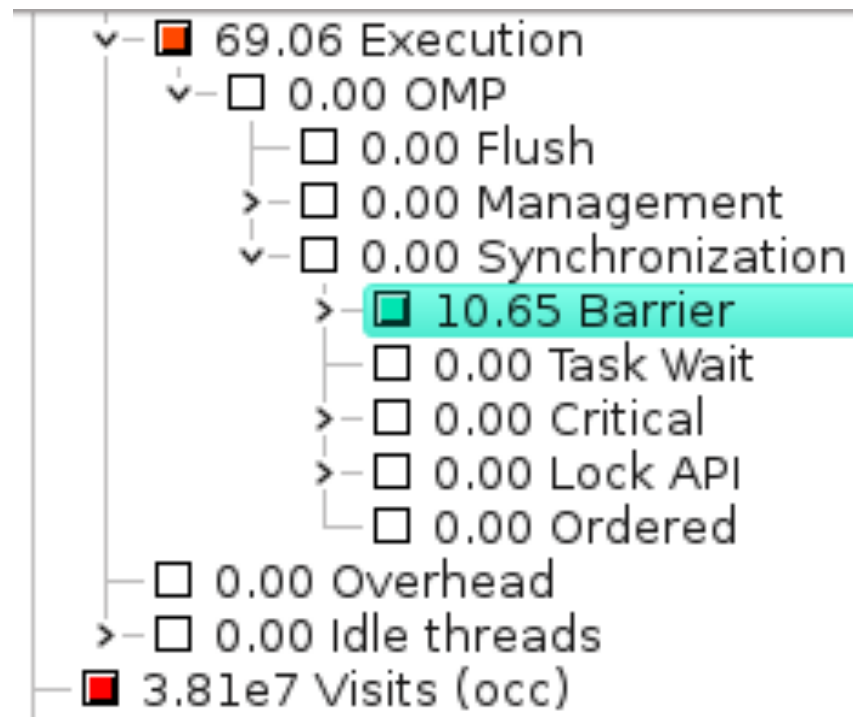
Imbalance – Test case



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Lulesh 2.0

- Insert imbalance in a parallel region via too large schedule clause



Imbalance – Call tree



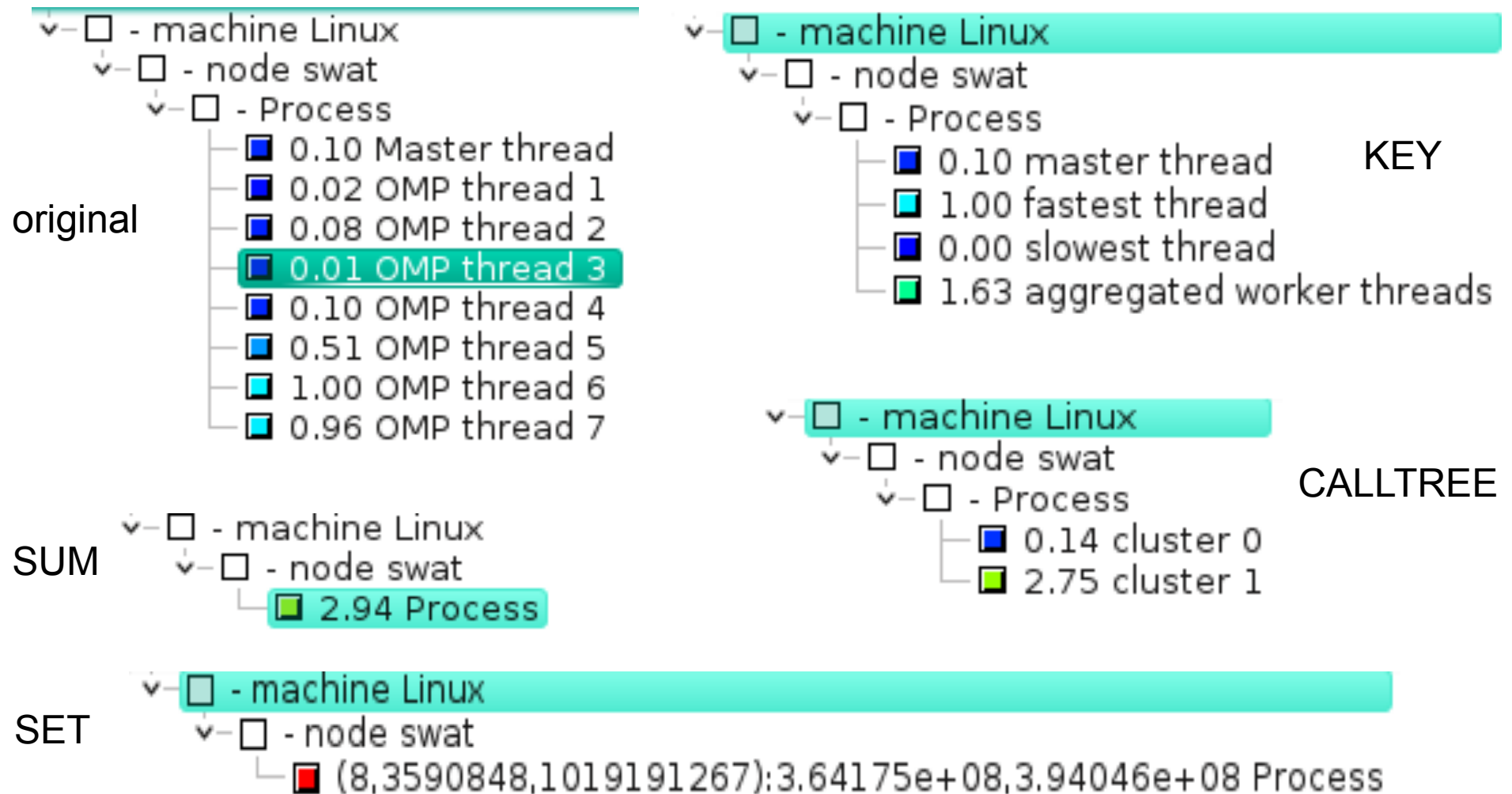
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```
>- 0.05 !$omp parallel @lulesh.cc:1216
>- 0.09 !$omp parallel @lulesh.cc:1240
v- 0.00 void CalcKinematicsForElems(Domain&, Real_t*, R
  v- 0.00 !$omp parallel @lulesh.cc:1538
    v- 0.00 !$omp for @lulesh.cc:1538
      - 0.00 Real_t CalcElemVolume(const Real_t*, co
        2.78 !$omp implicit barrier @lulesh.cc:1595
>- 0.07 !$omp parallel @lulesh.cc:1612
>- 0.29 !$omp parallel @lulesh.cc:1646
>- 0.55 !$omp parallel @lulesh.cc:1798
>- 0.03 !$omp parallel @lulesh.cc:2367
>- 1.14 !$omp parallel @lulesh.cc:2269
>- 0.31 !$omp parallel @lulesh.cc:2091
```

Imbalance – System trees



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Imbalance – Loop Body

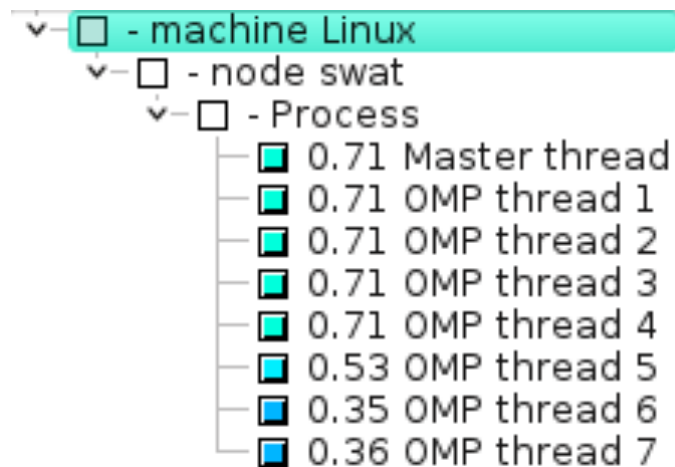


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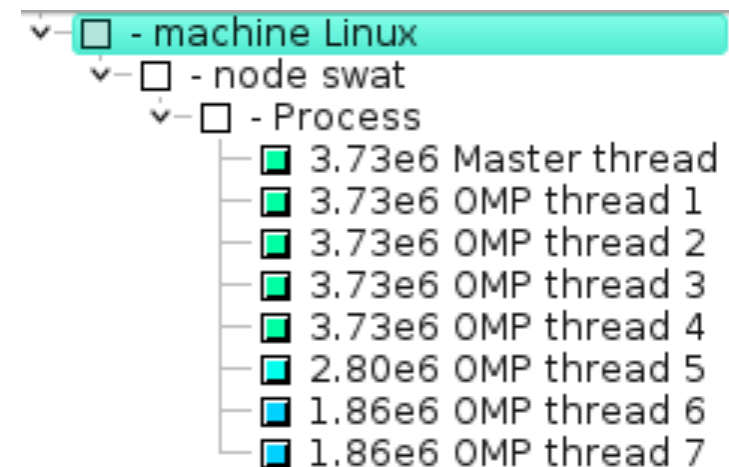
Correlate visits and execution time for the loop body

- Some threads have less iterations
- Same threads spend less time in loop

Execution time



Visits



Imbalance – Loop Body

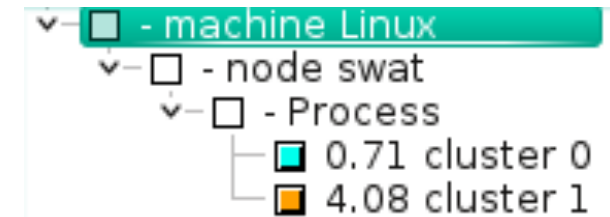
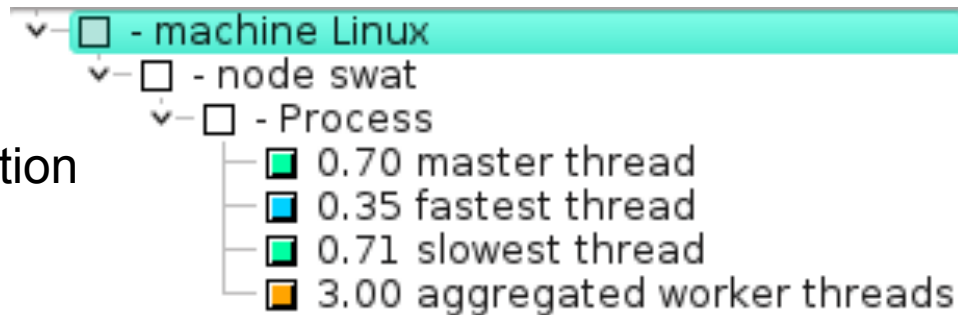


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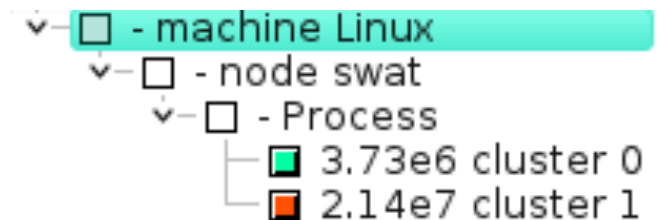
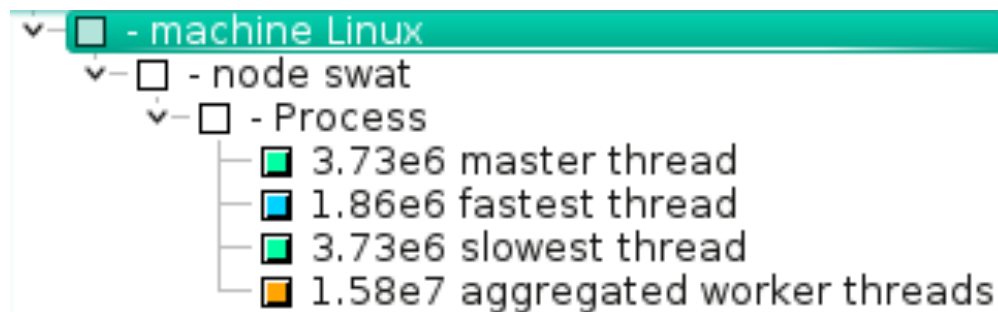
KEY

CALLTREE

Execution
time



Visits



Other test cases



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Task granularity

Lock contention

Memory bandwidth saturation

Per thread resolution less important

Imbalance is the hard case for thread aggregation

Conclusion

SUM:

- Best compression
- Good for analysis where thread resolution is not necessary

KEY:

- Possibility to find the most limiting bottleneck

SET:

- Similar compression to KEY
- Less correlation possibilities than KEY

CALLPATH:

- Non-optimal cluster criteria
- Promising approach



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Thank you for your attention!

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